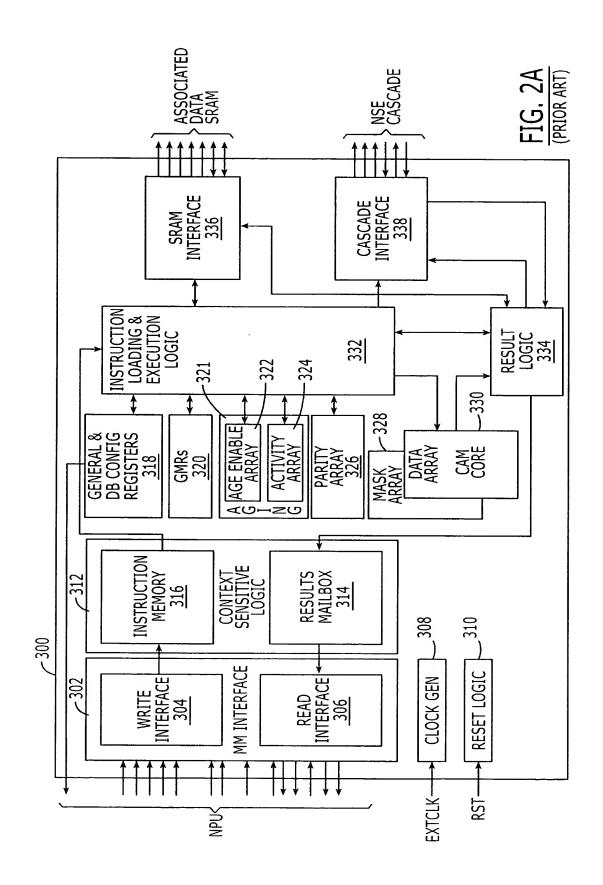


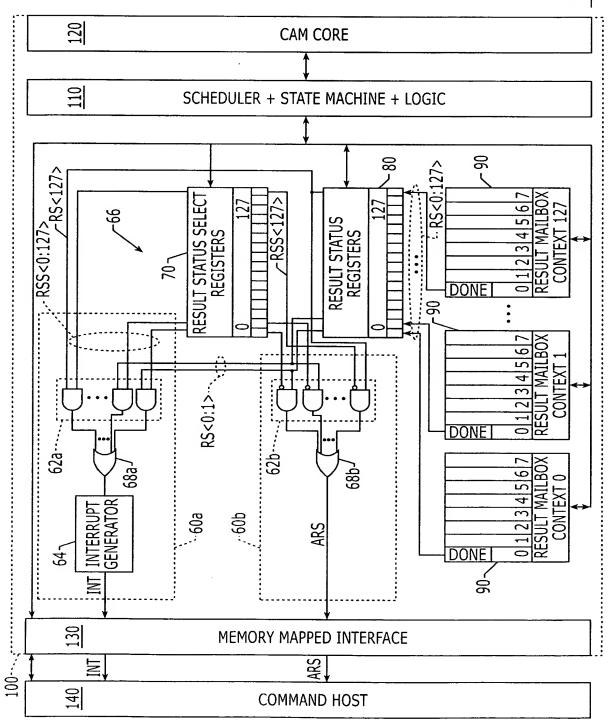
E

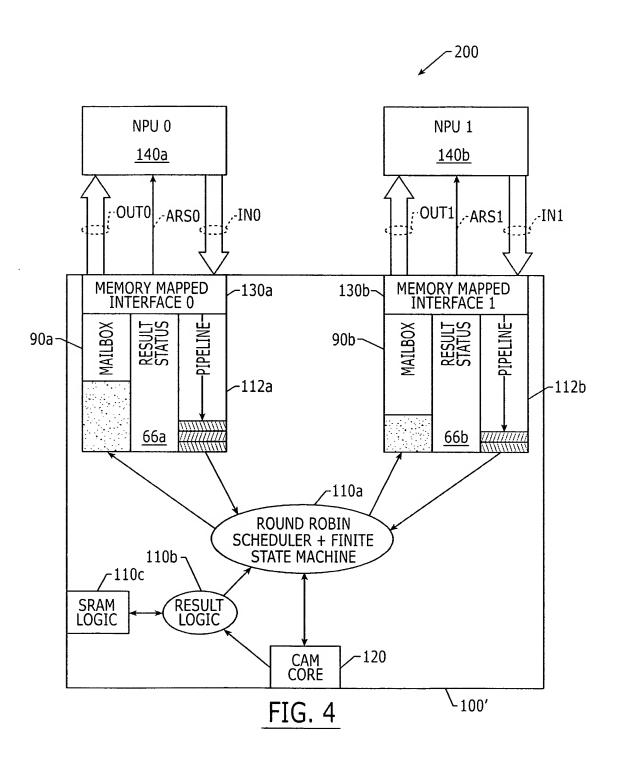


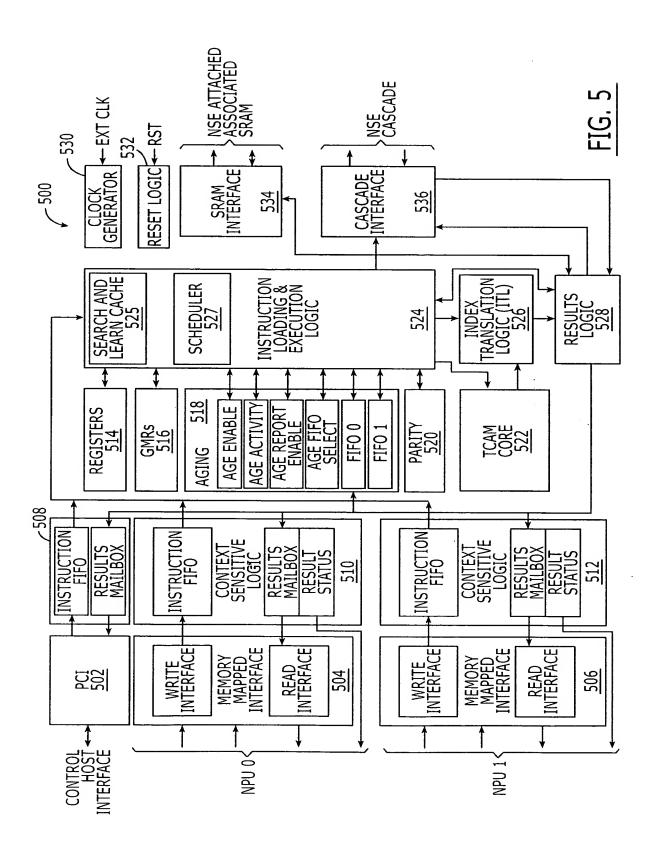
D TD		C 15 1 000 1
Dev ID	Segment	Segment Entry Offset

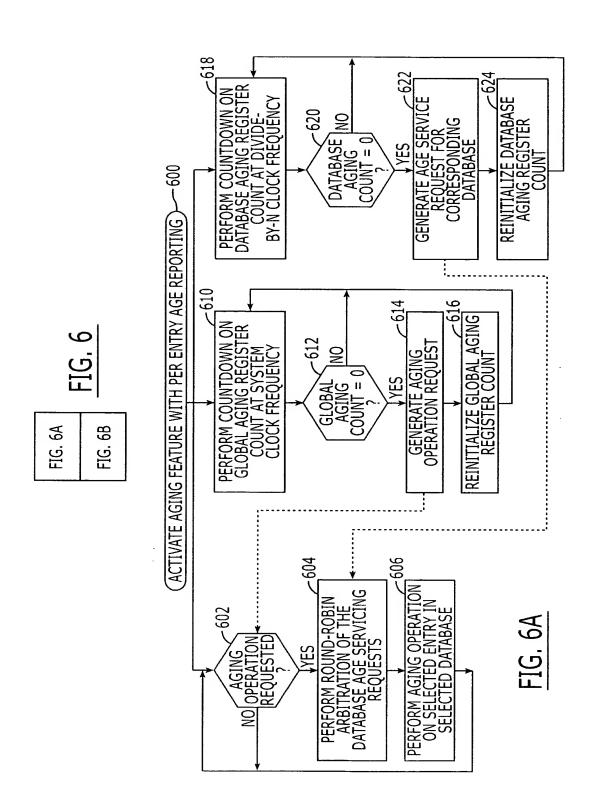
FIG. 2B

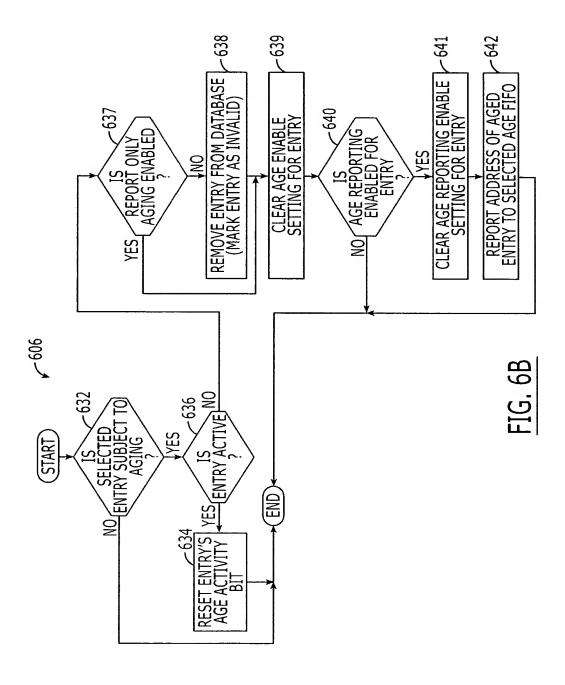
FIG. 3









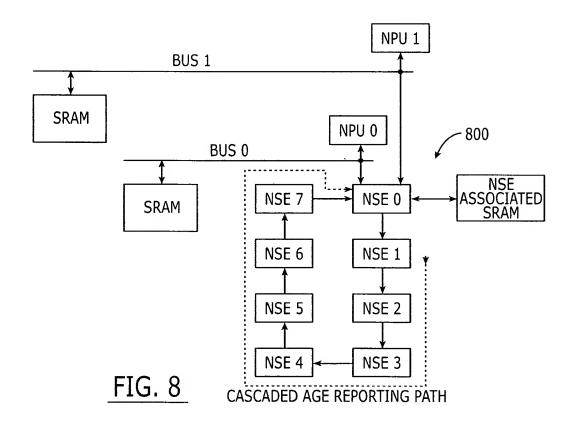


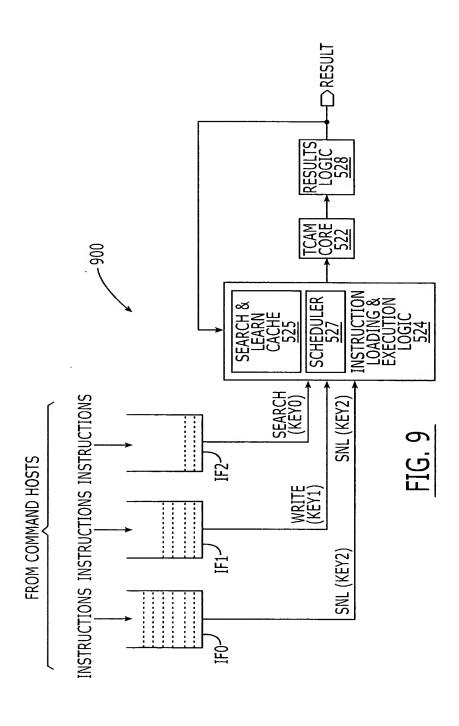
AGE FIFO SELECT SRAM ARRAY (8K x 32)	<u>700d</u>
AGE REPORT ENABLE SRAM ARRAY (8K x 32)	700c
AGE ACTIVITY SRAM ARRAY (8K x 32)	700b
	-
AGE ENABLE SRAM ARRAY (8K x 32)	700a

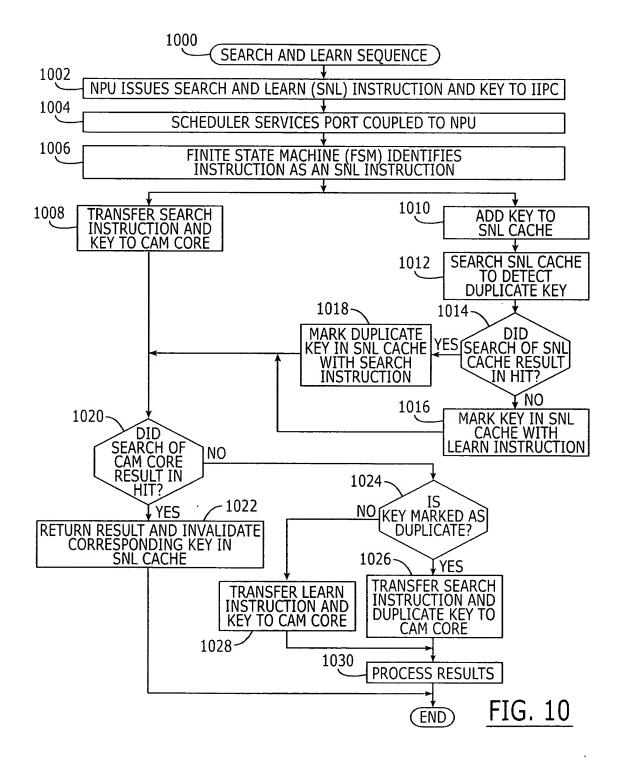
FIG. 7A

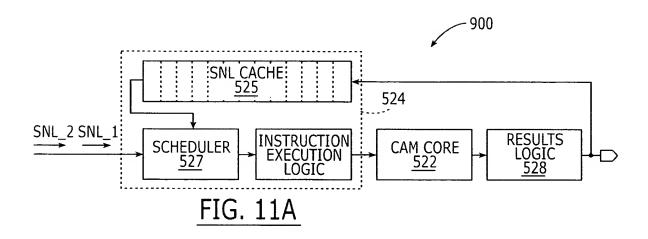
															700c
	←					32 COL	ς-								· 
	CAM ENTRY 0	CAM ENTRY 1	CAM ENTRY 2	•	•	•	•	•	•	۰	•	•	•	•	CAM ENTRY 31
	CAM ENTRY 32	CAM ENTRY 33	CAM ENTRY 34	۰	•	۰	۰	٠	•	•	۰	•	۰	•	CAM ENTRY 63
	CAM ENTRY 64	CAM ENTRY 65	CAM ENTRY 66	۰	۰	۰	٠	٠	•	•	٠	٠	۰	o	CAM ENTRY 95
	CAM ENTRY 96	CAM ENTRY 97	CAM ENTRY 98	• `	۰	۰	•	٠	٠	•	•	•	•	•	CAM ENTRY 127
	CAM ENTRY 128	CAM ENTRY 129	CAM ENTRY 130	۰	۰	0	•	۰	•	•	•	•	۰	•	CAM ENTRY 159
8192 ROWS 	CAM ENTRY 160	CAM ENTRY 161	CAM ENTRY 162	0	۰	۰	۰	•	•	٥	o	•	•	•	CAM ENTRY 191
	CAM ENTRY 192	CAM ENTRY 193	CAM ENTRY 194	0	•	۰	•	•	۰	0	•	•	۰	o	CAM ENTRY 223
									•						
	CAM ENTRY 262048	CAM ENTRY 262047	CAM ENTRY 262046	۰	•	۰	٥	۰	•	•	•	•	•	•	CAM ENTRY 262079
	CAM ENTRY 262080	CAM . ENTRY 262081	CAM ENTRY 262082	۰	۰	۰	۰	۰	۰	۰	۰	۰	۰	۰	CAM ENTRY 262111
	CAM ENTRY 262112	CAM ENTRY 262113	CAM ENTRY 262114	o	0	•	۰	۰	•	۰	•	•	۰	•	CAM ENTRY 262143
						7		7							

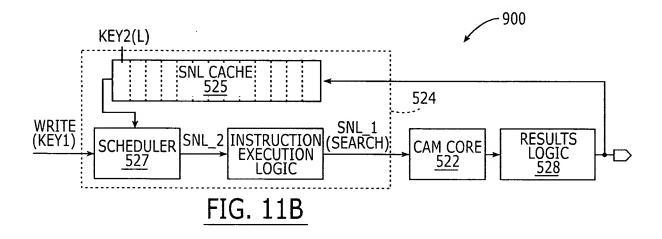
FIG. 7B

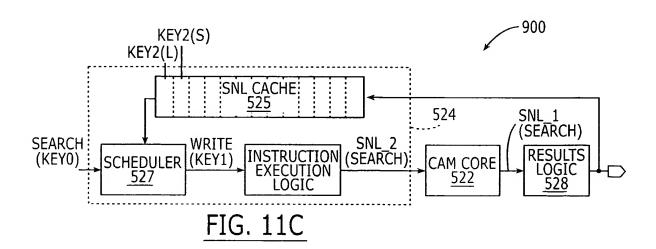


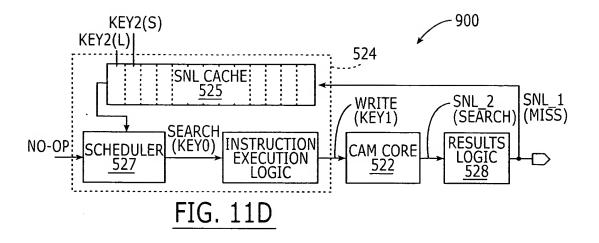


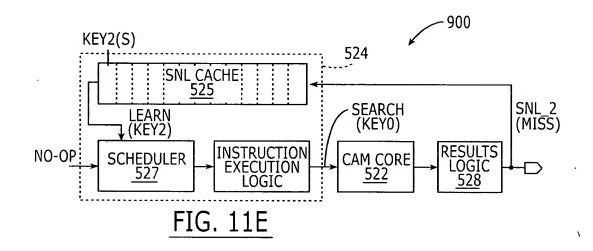


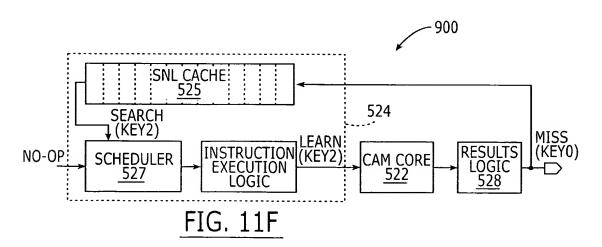


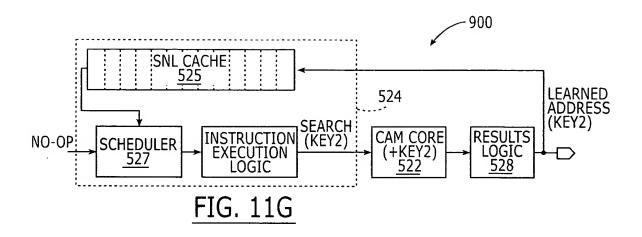


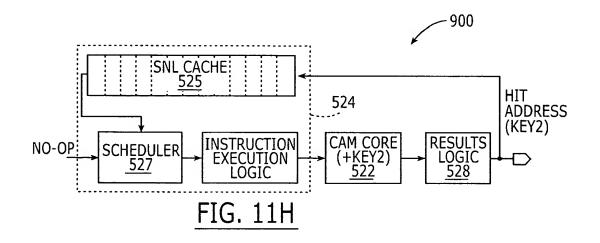


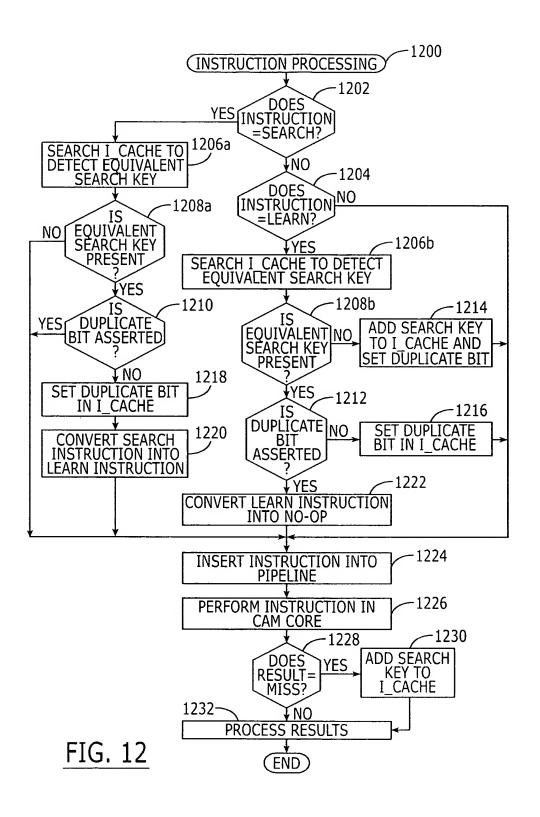


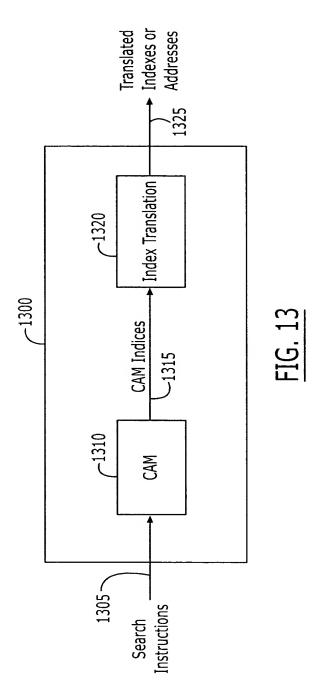


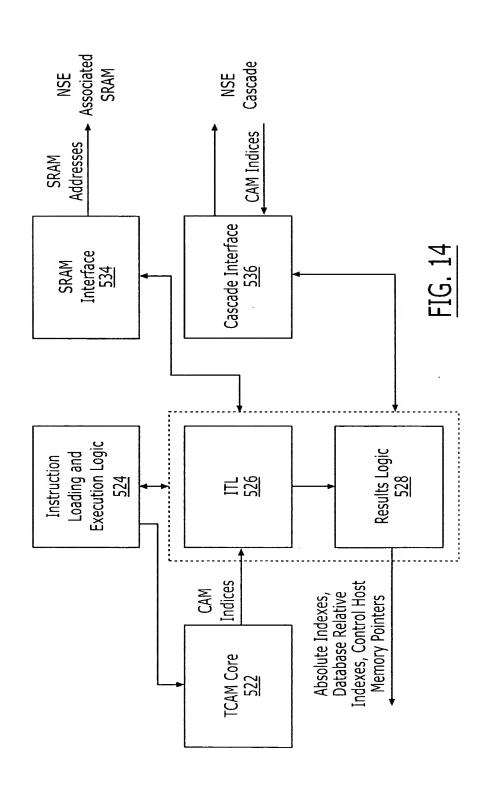












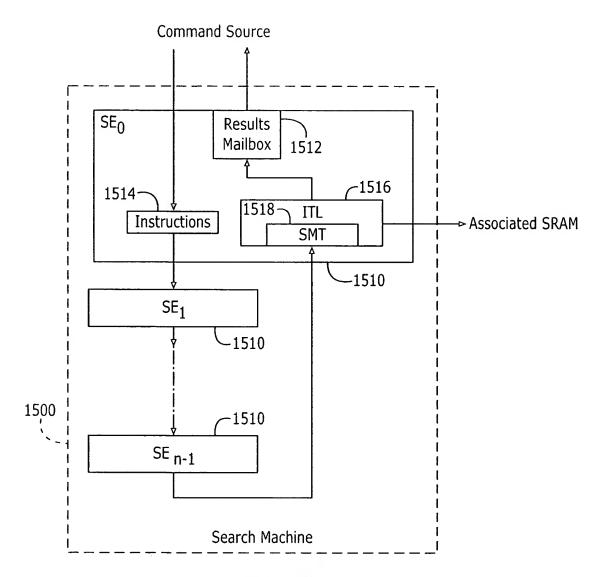


FIG. 15

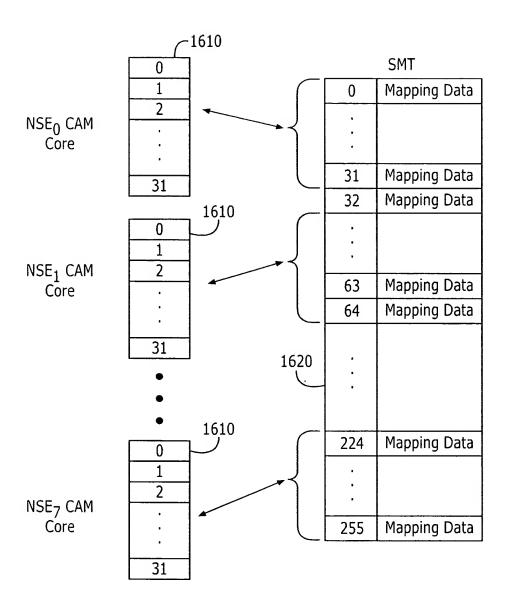


FIG. 16

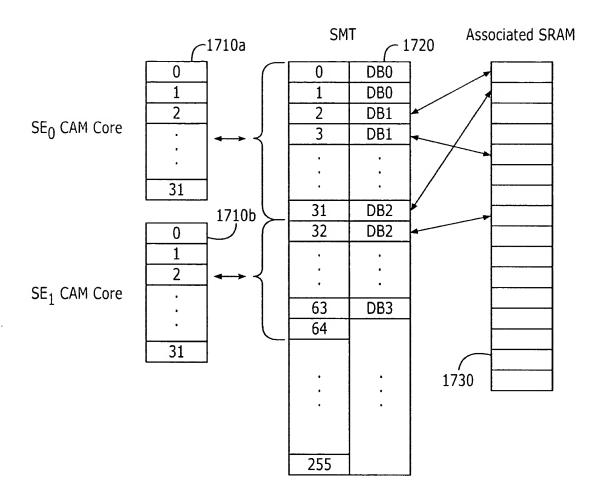


FIG. 17

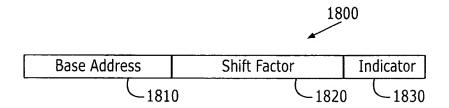


FIG. 18

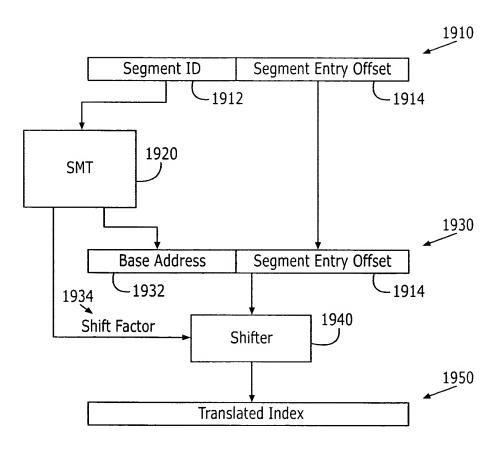


FIG. 19

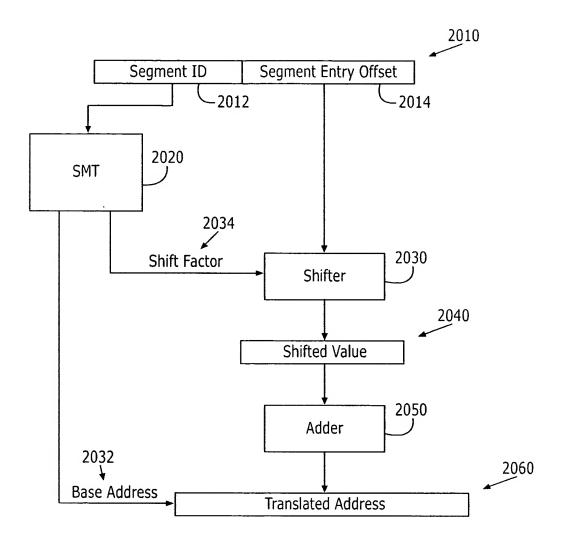


FIG. 20

Bits	24:6	5:2	1	0		
Field	BASE_ADDRESS	SHIFT_FACTOR	RSVD	RESULT_TYPE		

FIG. 21

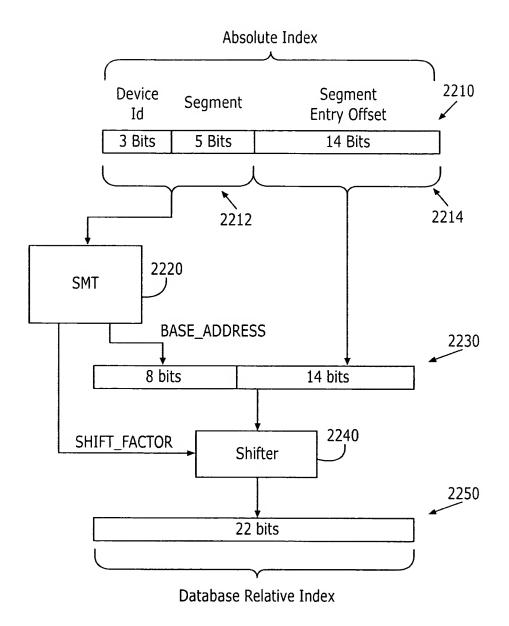


FIG. 22

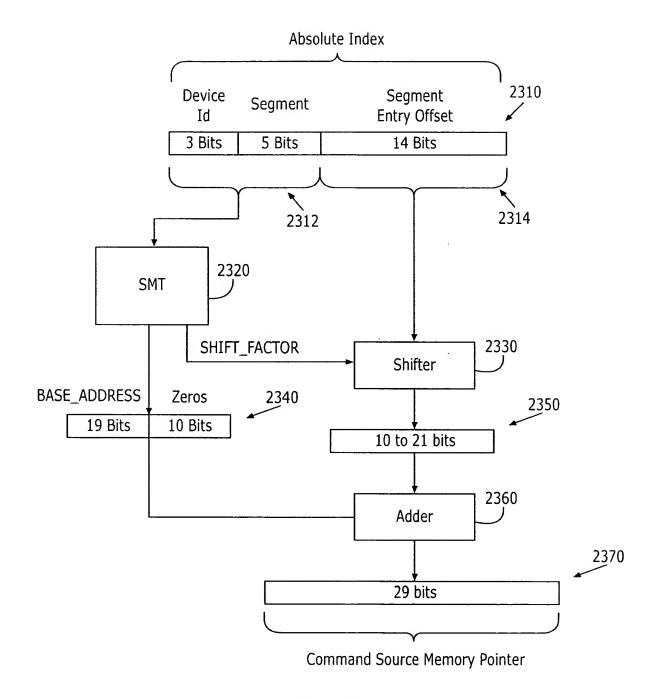


FIG. 23

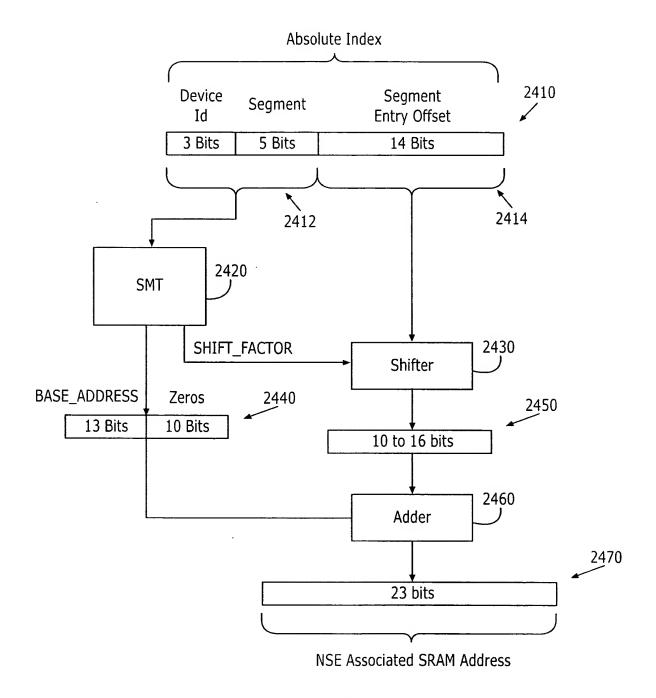


FIG. 24